



**AI HARDWARE
& EDGE AI
SUMMIT**

proteanTecs

Q&A WITH PROTEANTECS

WHY HAVE YOU CHOSEN TO PARTICIPATE AT THE SUMMIT?

ProteanTecs' participation in the AI Hardware Summit Conference for the 4th consecutive year is driven by its unwavering commitment to advancing the performance and reliability of electronic systems, encompassing high-performance computing and AI. Being a leading technology provider in advanced monitoring and analytics solutions, the conference serves as an ideal platform for proteanTecs to engage with companies that rely on complex compute systems, seeking to break the performance/power/reliability equation. As a premier event in the AI and HPC ecosystem, the conference offers the perfect opportunity for proteanTecs to remain at the forefront of the latest advancements, foster knowledge exchange, and establish valuable partnerships. The connections made and maintained during the summit have been instrumental in driving the company's growth year after year.

WHAT WILL PROTEANTECS BE SHARING WITH THE AUDIENCE?

The company will showcase its state-of-the-art solutions designed to enhance the performance, reliability, and efficiency of IT infrastructure across the cloud and edge. proteanTecs will present real-world use cases, case studies, and practical demonstrations to illustrate how its solutions can be seamlessly integrated into inference chips and advanced supercomputer processors. Additionally, the company will engage in knowledge-sharing sessions to discuss the latest advancements in data monitoring, data analytics, and predictive maintenance, offering valuable insights to the audience on how to optimize and ensure uptime, critical for HPC and supercomputing environments, allowing for power and performance monitoring and time-to-failure prediction.

CAN YOU GIVE INSIGHTS INTO YOUR SESSION ON "CLOUD RESILIENCY IN THE AGE OF HPC"?

The panel will explore how machine learning (ML) and data analytics can be leveraged to increase reliability and safety in automotive chips. Topics to be discussed include performance degradation monitoring, time-to-failure prediction, mission profile validation with usage feedback, and advanced root cause analysis. The panelists will address the opportunities and challenges associated with using ML for automotive chip safety and discuss potential solutions to optimize the operation of advanced electronics in cars. Using a hybrid strategy that combines traditional approaches and new data sources, attendees will gain insights into how ML-driven data can form the basis for fleet management and optimization, bridging the gap between hardware to software, production to usage, and telemetry to analytics, thus ensuring automotive systems' safe and reliable operation.

HOW DO YOU SEE THE FUTURE OF THE INDUSTRY CHANGING IN THE NEXT FIVE YEARS?

The industry is expected to witness significant advancements and transformations across various sectors, with a particular focus on HPC, AI, Data Servers, Software-defined vehicles, and chiplet-based architectures. In-field performance monitoring will become an industry norm, by equipping electronics with capabilities to report on their own health and performance. New techniques for in-situ and real-time tracking in mission-mode will enable predictive maintenance, failure prevention, power optimization, and pinpoint root cause analysis.

**REGISTER YOUR PLACE TO
MEET THE PROTEANTECS TEAM**